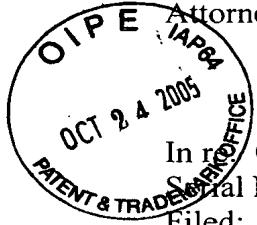


ITW



Attorney Docket No. 5649-877DV

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Cho et al.

Serial No.: 10/813,335

Filed: March 30, 2004

For: INTEGRATED CIRCUIT DEVICES HAVING DIELECTRIC REGIONS  
PROTECTED WITH MULTI-LAYER INSULATION STRUCTURES AND  
METHODS OF FABRICATING SAME

Group Art Unit: 2822

Examiner: Mark V. Prenty

Confirmation No.: 5111

Date: October 20, 2005

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

Applicants provide the present Amendment to address the issues raised in the Office Action mailed July 28, 2005.

If any extension of time for the accompanying response or submission is required, Applicants requests that this be considered a petition therefor. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 09-0461.

**Amendments to the Title** begin on page 2 of this paper.

**Amendments to the Specification** begin on page 3 of this paper.

**Amendments to the Claims** are reflected in the listing of claims, which begin on page 4 of this paper.

**Remarks/Arguments** begin on page 10 of this paper.

In re: Cho et al.  
Serial No.: 10/813,335  
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Page 2 of 11

**In the Title:**

Please amend the title in all occurrences as follows:

INTEGRATED CIRCUIT DEVICES HAVING DIELECTRIC REGIONS  
PROTECTED WITH MULTI-LAYER INSULATION STRUCTURES ~~AND METHODS OF~~  
~~FABRICATING SAME~~